

FPGA 10

FIG. 1

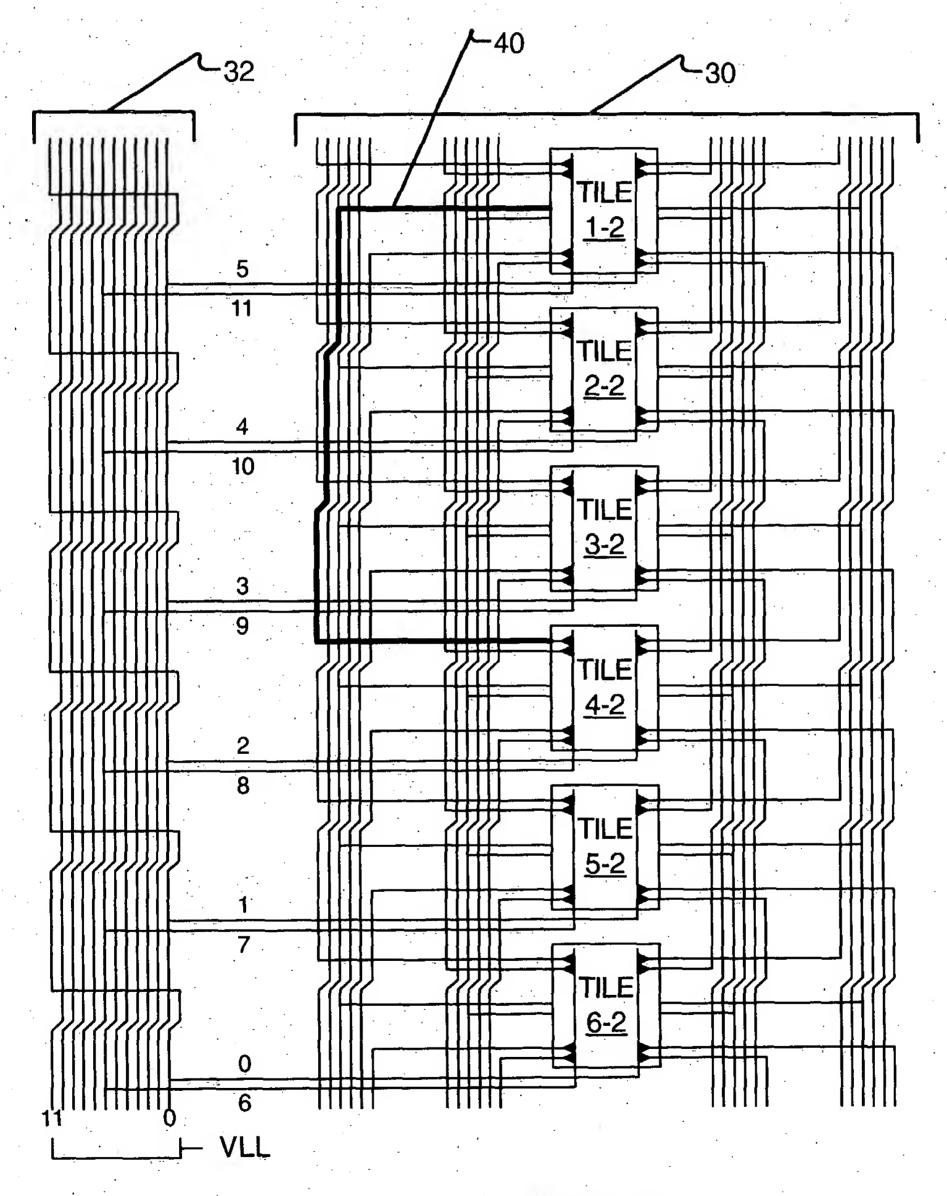
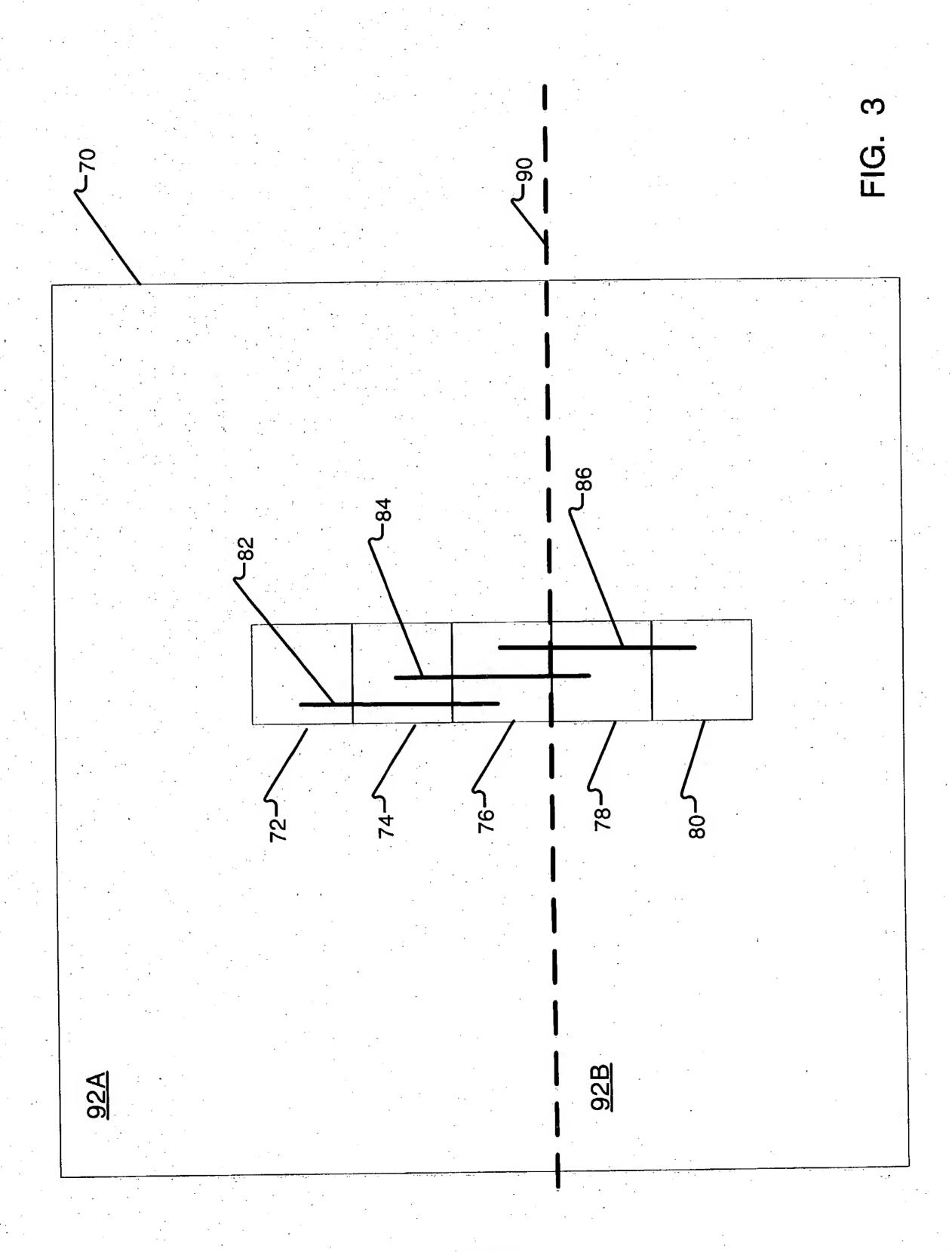
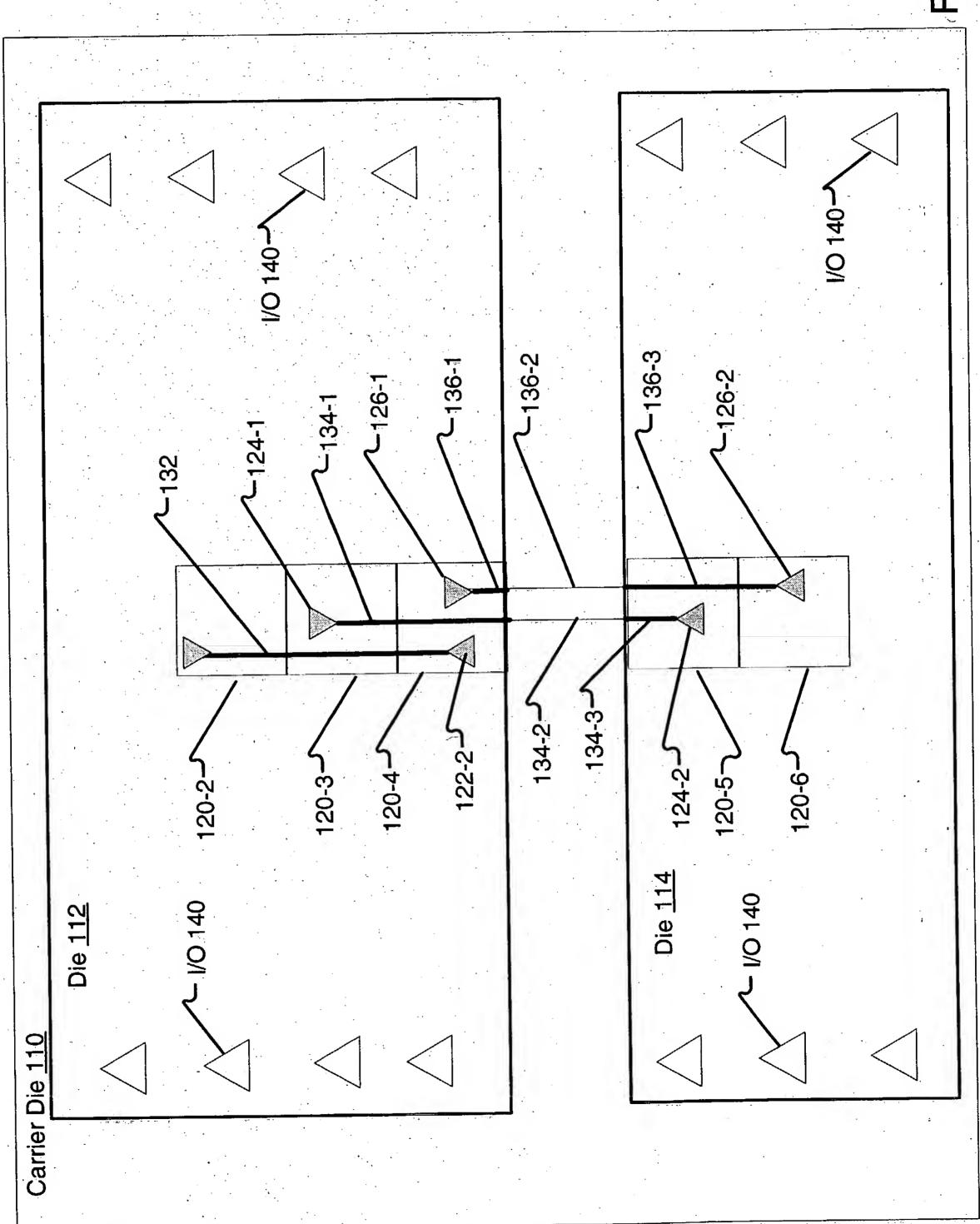


FIG. 2





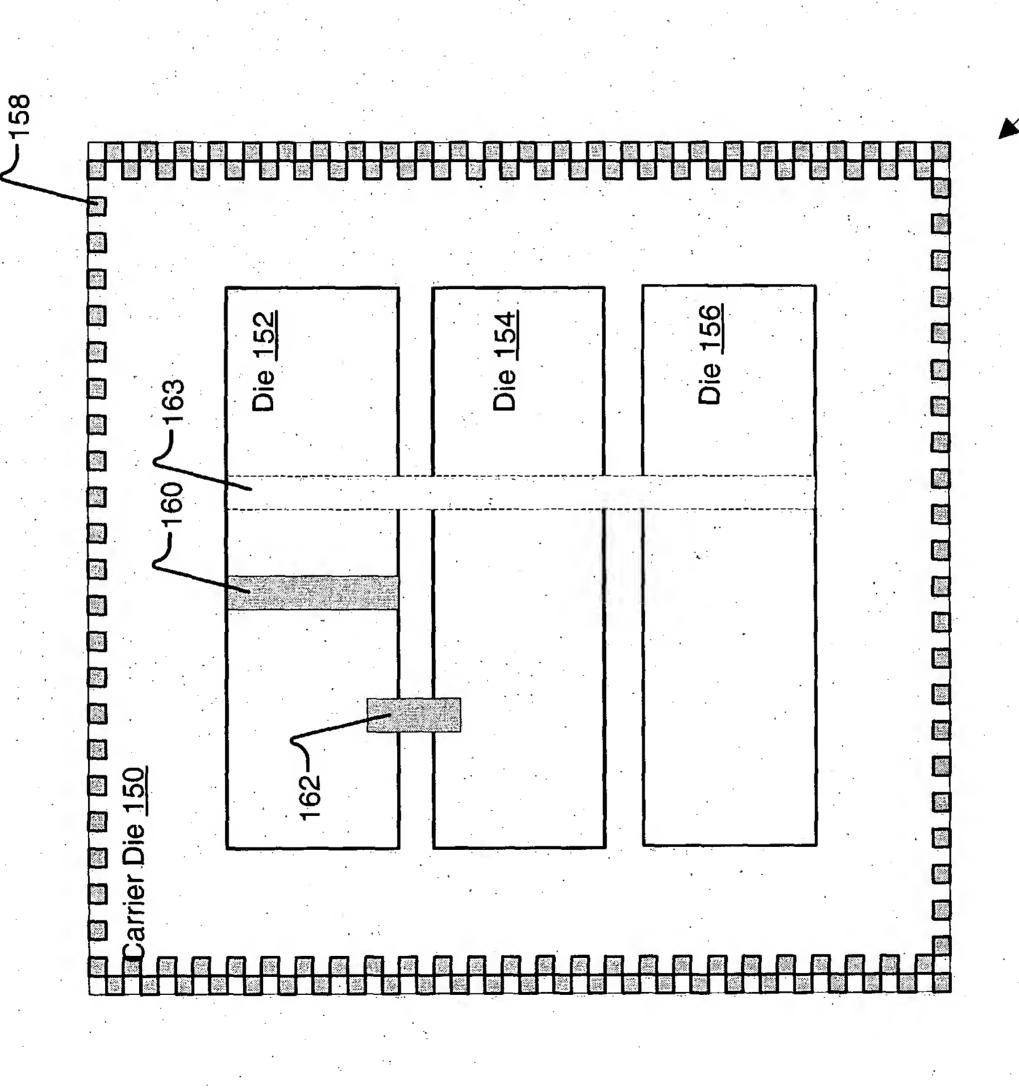
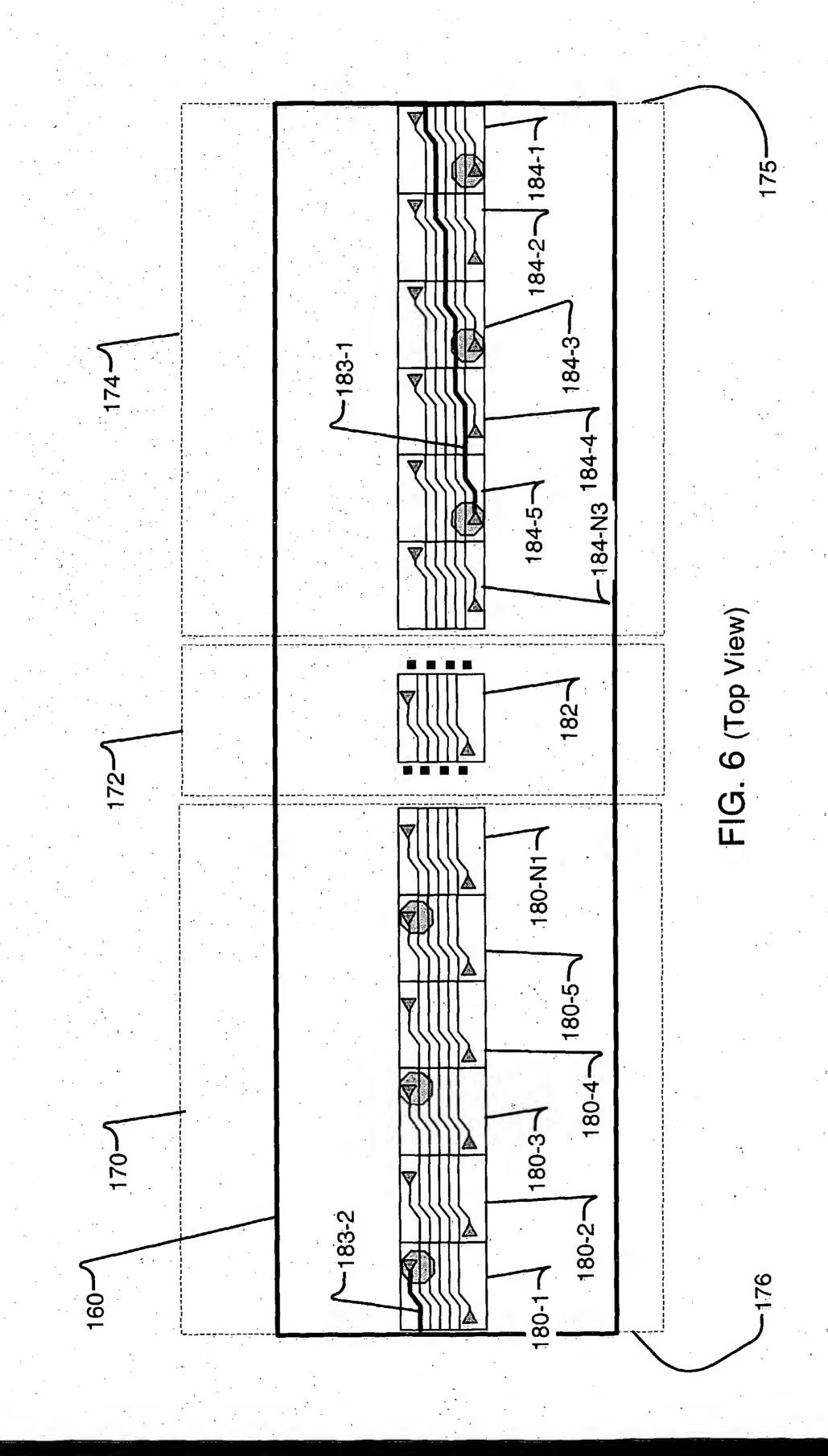


FIG. 5A

FIG. 5B



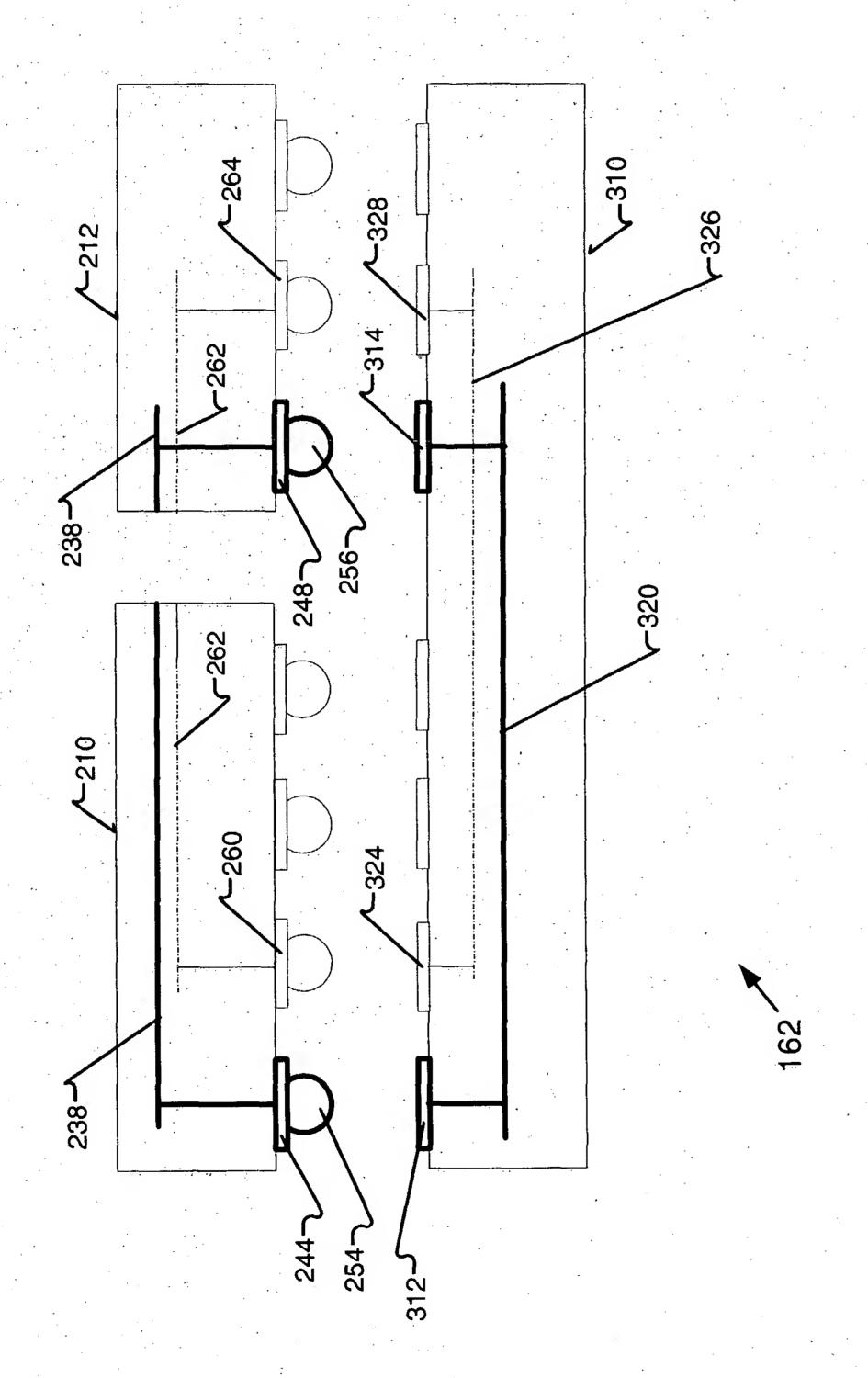


FIG. 7 (Side View)

